

BAR CODE LABEL



## U.S. PATENT APPLICATION

SERIAL NUMBER

07/869,851

FILING DATE

04/16/92

CLASS

324

GROUP ART UNIT

2313

APPLICANT

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## \*\*CONTINUING DATA\*\*\*\*\*

VERIFIED THIS APPLN IS A CON OF 07/458,507 12/28/89  
WHICH IS A DIV OF 07/140,628 01/04/88 PAT 4,916,389  
\_\_\_\_\_ WHICH IS A CON OF 06/562,969 12/19/83 , ,  
WHICH IS A CIP OF 06/368,162 04/14/82 PAT 4,482,985

## \*\*FOREIGN/PCT APPLICATIONS\*\*\*\*\*

VERIFIED JAPAN 56-220083 12/17/82  
JAPAN 56-57143 04/17/81  
\_\_\_\_\_ JAPAN 56-168698 10/23/81

STATE OR  
COUNTRY

JPX

SHEETS  
DRAWING

16

TOTAL  
CLAIMS

45

INDEPENDENT  
CLAIMS

7

FILING FEE  
RECEIVED

\$1,478.00

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TITLE

SEMICONDUCTOR INTEGRATED CIRCUIT

This is to certify that annexed hereto is a true copy from the records of the United States  
Patent and Trademark Office of the application as filed which is identified above.

By authority of the  
COMMISSIONER OF PATENTS AND TRADEMARKS

Date

Certifying Officer